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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inter patent application of

Yoshinori Uchiyama

Serial No.: 09/893,598

Group Art Unit: 2675

Filed: June 29, 2001

Examiner: Bell, Paul A.

RECEIVED

MAR 02 2004

Technology Center 2600

For: SEMICONDUCTOR CIRCUIT IN WHICH POWER CONSUMPTION IS  
REDUCED AND SEMICONDUCTOR CIRCUIT SYSTEM USING THE SAME

Honorable Commissioner of Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**EXCESS CLAIM FEE PAYMENT LETTER**

Sir:

Transmitted herewith is an amendment in the above-identified application. The fee has been calculated and is transmitted as shown below.

	<u>AFTER AMENDMENT</u>	<u>PREV. PAID FOR</u>	<u>EXTRA CLAIMS PRESENT</u>	<u>RATE</u>	<u>FEE DUE</u>
Total Claims	20 -	20	= 0	x \$18.00	\$ 0.00
Indep. Claims	4 -	3	= 1	x \$86.00	\$ 86.00
<b>TOTAL ADDITIONAL FEE FOR THIS AMENDMENT</b>					<b>\$ 86.00</b>

A check in the amount of \$86.00 to cover the excess claim fees and a duplicate copy of this sheet are enclosed. The Commissioner is authorized to charge any deficiencies in fees and credit any overpayment of fees to Attorney's Deposit Account No. 50-0481.

Respectfully Submitted,

Date:

2/25/04

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